



HLCache

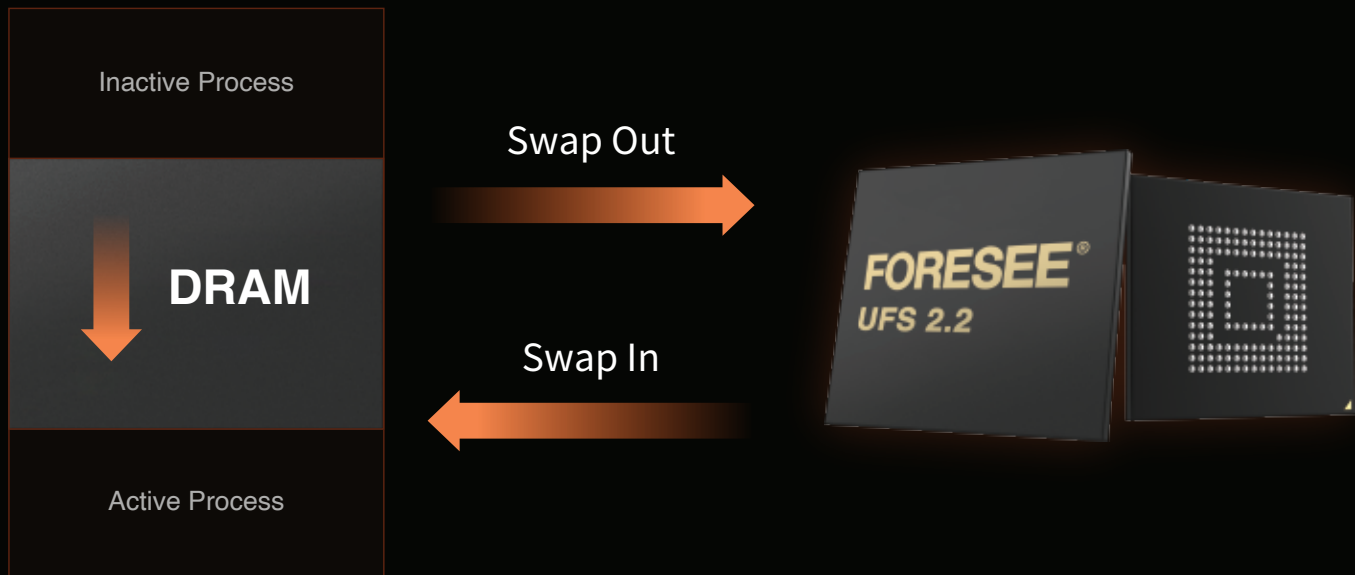
High Level Cache

**Embedded Integrated Innovation
Enables DRAM Capacity Reduction**



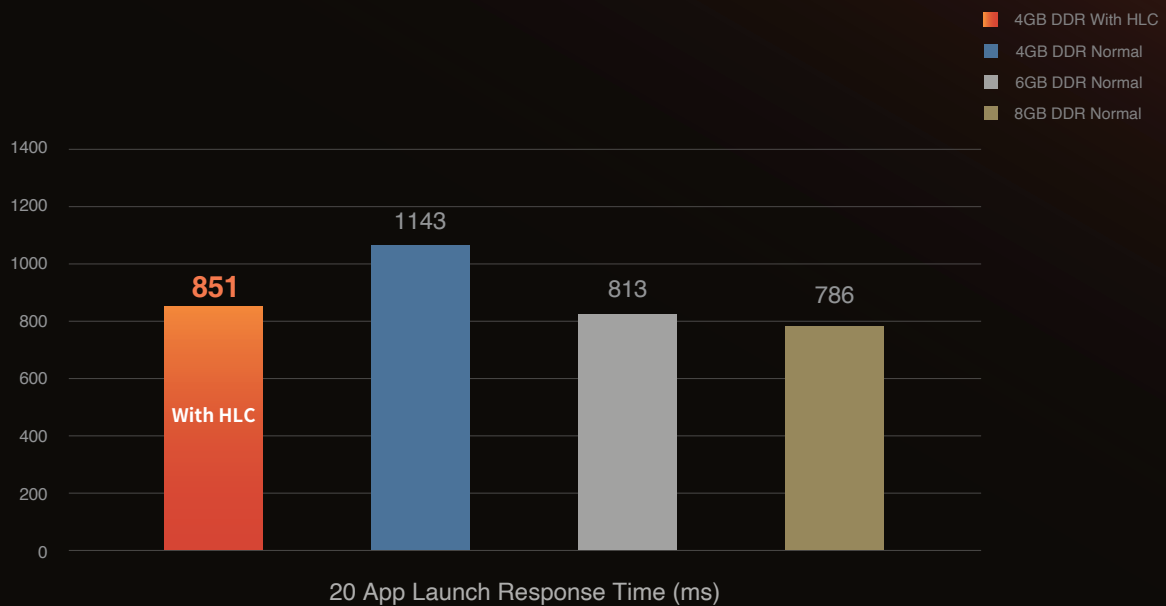
| UFS offloads warm and cold data from DRAM cache

Reducing device DRAM capacity to optimize BOM cost



| Uncompromised performance for Edge AI

Startup Response Performance Comparison



*Tested on the UNISOC chipset platform

High-performance controller ensures smooth operation



 **14nm Process Technology**

 **High Performance**

Sequential read / write >1000MB/s
Random read / write >210K IOPS

 **High Reliability**

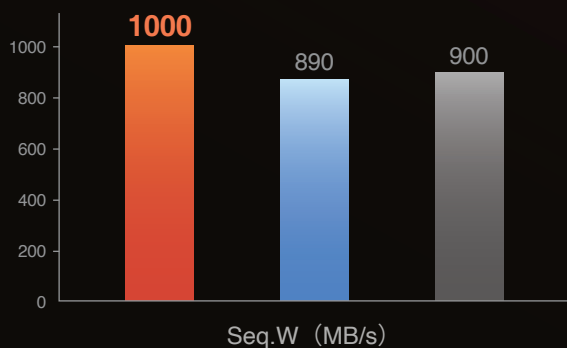
4KB LDPC | RAID | SECDED

 **Low Power Consumption**

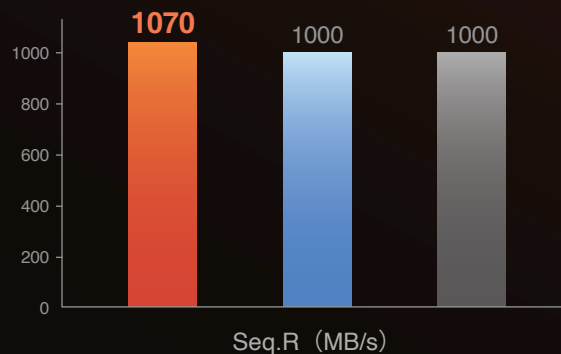
256GB Read and Write Performance

■ Longsys UFS 2.2
■ Mainstream A
■ Mainstream B

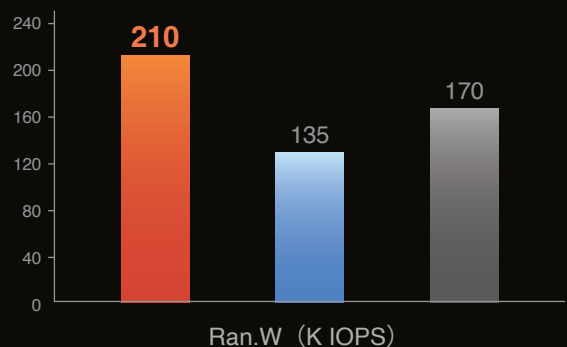
Sequential Write ↑ 11%



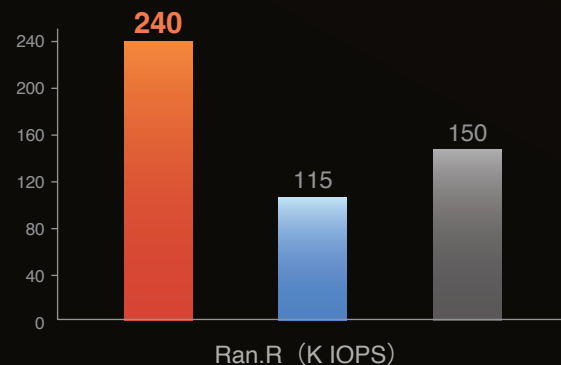
Sequential Read ↑ 7%



Random Write ↑ 55%



Random Read ↑ 108%



*The data comes from internal tests and algorithm estimates. Performance varies by device and environment.

STORAGE FOR AI



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