



SPU

Storage Processing Unit

5nm

 WiseMem

WM8500



Hyper-Converged Infrastructure

5nm Process Technology

PCIe 5.0 Interface

| SPU (Storage Processing Unit) for the AI Application Era

In-Memory Lossless Compression

- Average Compression Ratio
(Actual tests cover text, code, databases, and more)

2:1

- Ultra Reliable
(Bit rate range increased by 10%)
- High Speed and Low Latency
(Effective bandwidth of small-capacity drives doubled)

100GB ≈ 30USD

Ultra-Large Capacity

- Maximum Capacity Per Drive

128TB

- DRAM-less Design, Dynamic Mapping Table, Lower Cost & Same Speed

cSSD ≤ 8TB
Small Capacity

eSSD ≥ 128TB
High Cost

HLC Technology

- Warm and Cold Data Tiered Down to SSD

On the premise of ensuring a smooth experience, reduce memory capacity and lower terminal costs

DRAM↓

- Save DRAM Capacity

≈ 40%

Hybrid NAND

- Intelligent Hybrid Scheduling

TLC + QLC

- Performance-Cost Balance



Dynamic HMB

- Performance Comparable to DRAM-based
Mitigate DRAM Bit Flipping

HMB

Industry-Leading NAND I/O

- Supports High-Speed 4800MT/s
Optional 4-Channel or 8-Channel

4800MT/s

| Jointly Explore Edge AI Applications



Intelligence Storage Agent

KV Cache

KV Cache



HDD Upgrade Replacement

| Intelligent Storage Scheduling Engine — Edge-side iSA (Intelligent Storage Agent)



| Agent Joint Tuning

397B

Local Deployment of Large-Scale Models

256K

Ultra-Long Context (122B)

40%

Reduced DRAM Usage

HLCache

Optimized Specifically for AI

*The data comes from internal tests and algorithm estimates. Performance varies by device and environment.

STORAGE FOR AI



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